STATEMENT UNDER 37 CFR 3.73(b)	
Applicant/Patent Owner: Mar Hershenson, et al.	
Application No./Patent No: 09/843,486/7,065,727	Filed/Issue Date:April 25, 2001/June 20, 2006
Entitled: OPTIMAL SIMULTANEOUS DESIGN AND FLOORPLANNING OF INTEGRATED CIRCUIT	
Sabio Labs, Inc. , a	corporation
(Name of Assignee)	(Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)
states that it is:	
1. ☑ the assignee of the entire right, title, and interest; or	
2. an assignee of less than the entire right, title and interest.	
The extent (by percentage) of its ownership interest is %	
in the patent application/patent identified above by virtue of either:	
A. [] An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel, Frame, or for which a copy thereof is attached.	
OR	
B. [🗵] A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:	
1. From: Hassibi, Arash; Hentz, Andre; Hershenson Mar	To: Barcelona Design, Inc.
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[] Additional documents in the chain of title are listed on a supplemental sheet.	
[D] Copies of assignments or other documents in the chain of title are attached. [NOTE: A separate copy (i.e., a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, if the assignment is to be recorded in the records of the USPTO. See MPEP 302.08]	
The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.	
May J	October 3, 2007
Si gna ture Mark J. Itri	Date 949-851-0633
Printed or Typed Name	Telephone Number
Attorney for Applicant	